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**Tuesday, October 16, 11:30, Room “A”**

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**ANALOG IC's**  
***Session AnIC: Oral presentations***

Chair:

**F. Bauer**, ABB Switzerland Ltd, Daettwil, Switzerland  
**T. Visan**, Infineon Technologies Romania, Bucharest,  
Romania

**AnIC.1** *A NEW CIRCUIT TECHNIQUE FOR IMPROVING*  
**11:30** *TRANSIENT LOAD REGULATION IN LOW-*  
*VOLTAGE CMOS LDOS*, **C. Stanescu**, ON  
Semiconductor Romania S.R.L., Bucharest,  
Romania.

**AnIC.2** *CHARGE LOSS ACTIVATION DURING NON-*  
**11:50** *VOLATILES MEMORY DATA RETENTION*,  
**J. Postel-Pellerin**, **G. Micolau**, **P. Chiquet**,  
**R. Laffont**, **F. Lalande**, **J.-L. Ogier\***, IM2NP-  
CNRS, UMR, Aix-Marseille Univ., Marseille,  
\*STMicroelectronics, Rousset, France.

**AnIC.3** *OVERCOMING LEAKAGE CURRENT BY*  
**12:10** *OUTPUT PRECHARGING IN ERROR*  
*AMPLIFIERS*, **V. Anghel<sup>1,2</sup>**, **C. Bartholomeusz<sup>2</sup>**,  
**G. Pristavu<sup>1</sup>**, **G. Brezeanu<sup>1</sup>**, <sup>1</sup>“Politehnica” Univ. of  
Bucharest, <sup>2</sup>ON Semiconductor Romania SRL,  
Bucharest, Romania.

**AnIC.4** *COMPARATIVE ANALYSIS OF FOUR SECOND-*  
**12:30** *ORDER OA-RC POLYPHASE FILTERS FOR AN*  
*ISM LOW-IF RECEIVER*, **I. Campanu**, **T. Salajan**,  
**R. Onet**, **M. Neag**, Technical Univ. of Cluj-Napoca,  
Romania.

**AnIC.5** *SUBSTRATE LEAKAGE CURRENT INFLUENCE*  
**12:50** *ON BANDGAP VOLTAGE REFERENCES IN*  
*AUTOMOTIVE APPLICATIONS*, **L. Radoias,**  
**C. Zegheru, G. Brezeanu\***, Infineon Technologies  
Romania, Bucharest, \*Univ. "Politehnica" of  
Bucharest, Romania.

**AnIC.6** *NONIDEAL BEHAVIOR OF A CLASS OF*  
**13:10** *CELLULAR NEURAL NETWORKS*, **P.M. Puscasu<sup>1</sup>,**  
**L. Goras<sup>1,2</sup>,** <sup>1</sup>"Gheorghe Asachi" Technical Univ.,  
<sup>2</sup>Inst. of Computer Science, Iasi, Romania.

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**13:30–14:30 LUNCH**

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