

MODELLING

Session M: Oral presentations

Chair:

N. Marin, IMT Bucharest, Romania

D. Dobrescu, “Politehnica” University of Bucharest,
Romania

- M.1** *ANALYTICAL MODELING OF CONTACT*
15:00 *RESISTANCE IN ORGANIC TRANSISTORS,*
A. Bonea¹, T. Hassinen², B.A. Ofrim¹, D.C. Bonfert³, P. Svasta¹, ¹“Politehnica” Univ. of Bucharest, Romania, ²VTT Technical Research Center of Finland, Espoo, Finland, ³Fraunhofer Research Inst. for Modular Solid State Technologies, Munich, Germany.
- M.2** *COMMON ANODE AMPLIFIER OF A*
15:20 *COMMERCIAL MOS TRANSISTOR IN AVALANCHE GATED DIODE CONFIGURATION,*
A. Rusu, L. Dobrescu, M. Enachescu*, A. Rusu, C. Burileanu, Univ. “Politehnica” of Bucharest, Romania, *TU Delft Univ. of Technology, The Netherlands.
- M.3** *A SYSTEMVERILOG APPROACH IN SYSTEM*
15:40 *VALIDATION WITH AFFINE ARITHMETIC, R.-C. Mialtu,* Infineon Technologies Romania, Bucharest, Romania.
- M.4** *OPTIMIZATION OF A PASSIVE MICROMIXER*
16:00 *USING MODELS BASED ON VARIABLE DIFFUSION COEFFICIENT, O.T. Nedelcu, I. Stanciu,* IMT Bucharest, Romania.
- M.5** *NEAR-FIELD ENHANCEMENT OF A ROD-LIKE*
16:20 *NANOANTENNA: ELCTROSTATIC VERSUS FULLY RETARDED RESULTS, T. Sandu, V. Buiculescu,* IMT Bucharest, Romania.

M.6 *TIME MODULATION–THE EXPONENTIAL WAY,*
16:40 **G. Pristavu**¹, **C. Bartholomeusz**², **V. Anghel**^{1,2},
G. Brezeanu¹, ¹”Politehnica” Univ. of Bucharest,
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