

# MICRO AND NANOTECHNOLOGIES BULLETIN

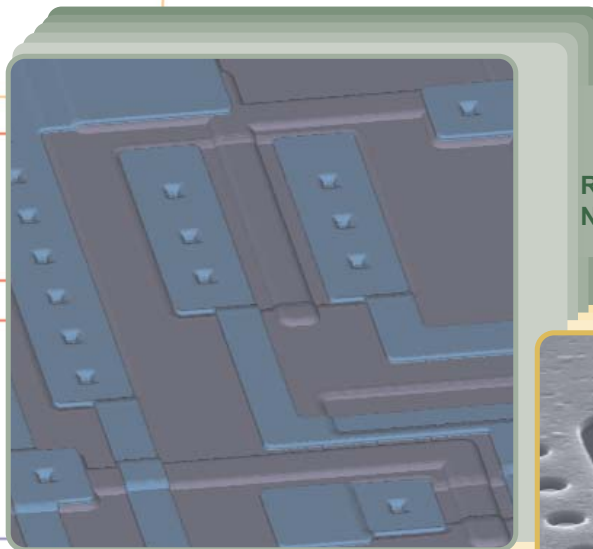
Published quarterly by:  
ROMINFOR/Micro and Nano  
Science and Technology

VOL.8/No.4

December 2007

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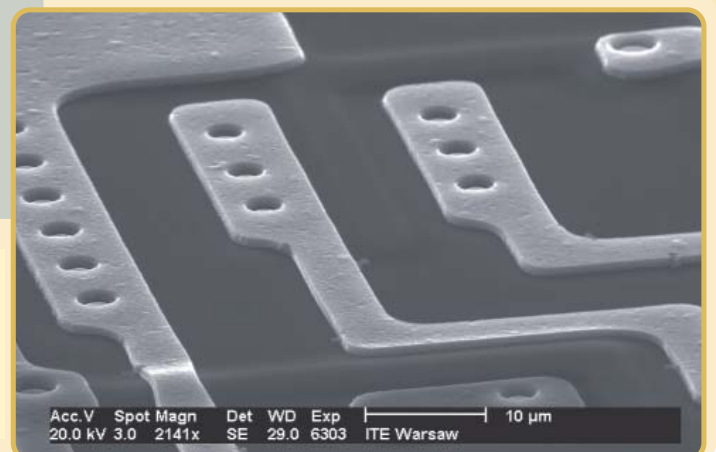


The process emulation approach at ITE uses 3D numerical simulation to produce highly accurate models of the effects of chemical reactions involved in fabrication.

More details on page 4.

Emulation of the ITE CMOS fabrication process  
(left) and SEM image of the structure (right)

Results obtained by Division of Silicon Microsystem and  
Nanostructure Technology, ITE, Warsaw, Poland



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